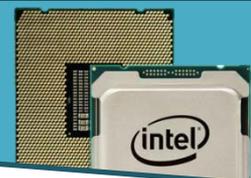


GCSE Computer Science

Topic 1.1 Systems Architecture

Von Neumann designed the first 'stored program' computer, where data and instructions are stored **together** in memory.



A computer systems is a combination of **hardware** and **software** that work together to process data.

Hardware: physical components that make up a computer system.

Software: programs/applications that run on a computer.

An **embedded system:** a dedicated system built into a larger system to control it.



Embedded systems are restricted by price and physical size. They are not programmable.

- Benefits of embedded systems:**
- ✓ Easier to design.
 - ✓ Cheaper to produce.
 - ✓ More **efficient** at doing their job.

- The **control Unit** is in overall control of the CPU.
- Its main job is to execute instructions by following the FDE cycle.
- It also controls the flow of data inside and outside the CPU.
- The control unit contains the decoder.

- The **Arithmetic Logic Unit** performs all the calculations.
- It completes arithmetic operations (+, -, *, /).
- It also completes logic operations using logic gates.
- The ALU contains the accumulator register.

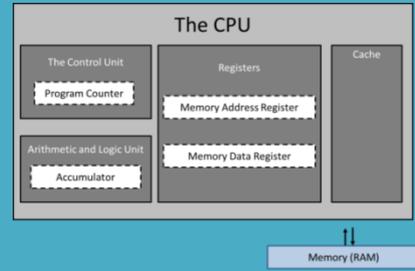
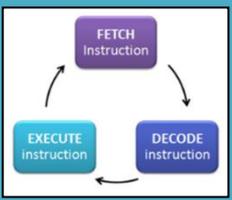
- **Cache** is very fast memory.
- It is slower than the registers but faster than RAM.
- It stores regularly used data so that the CPU can access it quickly next time it is needed.
- If the data is not in cache, it will be fetched from RAM.
- Caches have very low capacity and are expensive compared to RAM.

Registers are the smallest & fastest type of memory in a computer system. **They hold data the CPU needs immediately.**



The **PC** holds the address of the next instruction to be fetched.

The **Accumulator** holds the results of any calculations done by the ALU.



The **Central Processing Unit** processes data and instructions following the FDE cycle.

The **MAR** holds the address of the memory location to fetch the data from / write to for the current cycle.

The **MDR** holds the data fetched from memory.

The CU copies the memory address from the Program Counter to the MAR.

The CU requests the instruction or data stored in the address held in the MAR, to be brought to the MDR.

The CU updates the Program Counter with the address of the next instruction, ready for the next cycle.

The instruction in the MDR is decoded by the Control Unit(decoder).

The CU carries out the instruction using the ALU for any calculations (and stores the results in the Accumulator).

Clock Speed: the number of instructions single processor can process per second. The **HIGHER** the clock speed, the faster the data can be processed.

Number of cores: each core in the CPU can process data independently. The **MORE** cores, the **FASTER** the data can be processed.

The **cache** is memory inside the CPU. The **MORE** cache, the faster data can be fetched to be processed.



